

ISO/IEC 14443-4:2018 (E)

Cards and security devices for personal identification — Contactless proximity objects — Part 4: Transmission protocol

Contents

	Foreword
	Introduction
1	Scope
2	Normative references
3	Terms and definitions
4	Symbols, abbreviated terms and notation
4.1	Symbols and abbreviated terms
4.2	Notations
5	Protocol activation of PICC Type A
5.1	Activation sequences
5.2	Request for answer to select
5.3	Answer to select
5.3.1	Structure of the bytes
5.3.2	Length byte
5.3.3	Format byte
5.3.4	Interface byte TA(1)
5.3.5	Interface byte TB(1)
5.3.6	Interface byte TC(1)
5.3.7	Historical bytes
5.4	Protocol and parameter selection request
5.4.1	Start byte
5.4.2	Parameter 0
5.4.3	Parameter 1
5.5	Protocol and parameter selection response
5.6	Activation frame waiting time
5.7	Error detection and recovery
5.7.1	Handling of RATS and ATS
5.7.1.1	PCD rules
5.7.1.2	PICC rules
5.7.2	Handling of PPS request and PPS response
5.7.2.1	PCD rules
5.7.2.2	PICC rules
5.7.3	Handling of the CID during activation
6	Protocol activation of PICC Type B
7	Half-duplex block transmission protocol
7.1	Elements and mechanisms
7.2	Block format
7.2.1	Length field
7.2.2	Prologue field
7.2.2.1	Protocol control byte field
7.2.2.2	Card identifier field
7.2.2.3	Node address field
7.2.3	Information field
7.2.4	Epilogue field
7.3	Frame waiting time

7.4	Frame waiting time extension
7.5	Power level indication
7.6	Protocol operation
7.6.1	S(PARAMETERS) blocks
7.6.2	Multi-Activation
7.6.3	Chaining
7.6.4	Block numbering rules
7.6.4.1	PCD rules
7.6.4.2	PICC rules
7.6.5	Block handling rules
7.6.5.1	General rules
7.6.5.2	PCD rules
7.6.5.3	PICC rules
7.6.6	PICC presence check
7.6.6.1	Method 1
7.6.6.2	Method 2
7.6.7	Error detection and recovery
7.6.7.1	Errors detected by the PCD
7.6.7.2	Errors detected by the PICC
8	Protocol deactivation of PICC Type A and Type B
8.1	Deactivation frame waiting time
8.2	Error detection and recovery
9	Activation of bit rates and framing options in PROTOCOL state
10	Frame with error correction
10.1	General
10.2	Type A PCD frame format for bit rates up to $fc/16$ and higher than $fc/2$ and Type A PICC frame format for all bit rates
10.3	Type A PCD frame format for bit rates of $fc/8$, $fc/4$ and $fc/2$ and Type B PCD and PICC frame format for all bit rates
10.4	Enhanced block with error correction
10.4.1	General
10.4.2	Modified Hamming sub-block format
10.4.3	Hamming control byte
10.4.4	Hamming control generation matrix A
10.4.5	Hamming control bits calculation
10.4.6	Hamming control check matrix H
10.4.7	Error correction
10.5	Activation of frame with error correction in PROTOCOL state
Annex A (informative) Multi-Activation example	
Annex B (informative) Protocol scenarios	
B.1	General
B.2	Notation
B.3	Error-free operation
B.3.1	Exchange of I-blocks
B.3.2	Request for waiting time extension
B.3.3	DESELECT
B.3.4	Chaining
B.3.5	PICC Presence check
B.3.6	Exchange of additional parameters
B.4	Error handling
B.4.1	Exchange of I-blocks
B.4.2	Request for waiting time extension
B.4.3	DESELECT
B.4.4	Chaining
Annex C (informative) Block and frame coding overview	
Annex D (deliberately left blank)	

Annex E (informative) CRC_32 encoding

E.1 CRC_32 encoding

E.2 Code sample written in ANSI C language for CRC_32 calculation

Annex F (informative) Frame with error correction

F.1 Example

F.2 Code sample written in ANSI C language for Hamming control byte calculation

Annex G (informative) Framing options

Page count: 55