ISO 20794-6:2020 (E)

Road vehicles — Clock extension peripheral interface (CXPI) — Part 6: Transport and network layer conformance test plan

Contents

Foreword

Introduction

1 Scope

2 Normative references

3 Terms and definitions

4 Symbols and abbreviated terms

4.1 Symbols
4.2 Abbreviated terms

5 Conventions

6 General test specification considerations

6.1 General
6.2 Test conditions
6.3 IUT requirements
6.4 CTC definition
6.5 Test system set-up
6.6 Configuration of test system and IUT
6.6.1 General
6.6.2 IUT-specific set-up parameters
6.6.3 User_Specific configurations
6.6.4 T_FltMng1 configurations
6.6.5 T_FltMng2 configurations
6.6.6 N_ErrDet1 configurations
6.6.7 N_ErrDet2 configurations
6.6.8 W/S configurations
6.7 SUT initialisation
6.7.1 General
6.7.2 Default initialisation

7 Transport layer conformance test plan

7.1 General
7.2 4.CTC_1.1 – Master node – T_PDU and T_PCI data handling – T_PCItype and T_PCI_DL
7.3 4.CTC_1.2 – Slave node – T_PDU and T_PCI data handling – T_PCItype and T_PCI_DL
7.4 4.CTC_1.3 – Master node – T_PDU and T_PCI data handling (T_PCItype and T_PCI_DLext)
7.5 4.CTC_1.4 – Slave node – T_PDU and T_PCI data handling (T_PCItype and T_PCI_DLext)
7.6 4.CTC_1.5 – Master node – T_PDU and T_PCI data handling (T_PCItype and T_PCI_DLext with short data field)
7.7 4.CTC_1.6 – Slave node – T_PDU and T_PCI data handling (T_PCItype and T_PCI_DLext with short data field)
7.8 4.CTC_2.1 – Master node – TL Error detection test (T_PCItype)
7.9 4.CTC_2.2 – Slave node – TL Error detection test (T_PCItype)
7.10 4.CTC_2.3 – TL Error detection test (T_PCItype) using error bit
7.11 4.CTC_2.4 – Master node – TL Error detection test (T_PCI_DL)
7.12 4.CTC_2.5 – Slave node – TL Error detection test (T_PCI_DL)
7.13 4.CTC_2.6 – TL Error detection test (T_PCI_DL) using error bit

7.14 4.CTC_2.7 – Master node – TL Error detection test (T_PCI_DLext)
7.15 4.CTC_2.8 – Slave node – TL Error detection test (T_PCI_DLext)
7.16 4.CTC_2.9 – TL Error detection test (T_PCI_DLext) using error bit

8 Network layer conformance test

8.1 General
8.2 3.CTC_3.1 – Master node – N_PDU specification test (N_NAD)
8.3 3.CTC_3.2 – Slave node – N_PDU specification test (N_NAD)
8.4 3.CTC_3.3 – Reception logic test (in the case of T_PID = 1F16 and N_NAD = 0016)
8.5 3.CTC_4.1 – Master node – NL error detection (N_As timeout)
8.6 3.CTC_4.2 – Slave node – NL error detection (N_As timeout)

Page count: 30